

EAST - (098551c:1)

File View Edit Tools Window Help

☐ Drafts
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☒ Active
 L1: (24069) test\$4 near9(delay or timing)
 L3: (17071) correct\$4 or modif\$5)same compar\$4 same(delay or timing
 L16: (17126) correct\$4 or modif\$5)same based same(delay or timing)same(delay or timing
 L18: (49693) circuit near9 modif\$5
 L19: (245) 11 and 13 and 116
 L20: (40) 118 and 119
 L21: (8) 716/1-21.ccls. and 120
☐ Failed
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☐ UDC
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	U	I	Document ID	Iss	Title	Current	Ci
1	<input type="checkbox"/>	<input type="checkbox"/>	US 6370675 B1	20	Semiconductor integrated circuit design and evaluation syste	716/6	71
2	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6327556 B1	20	AT-speed computer model testing methods	703/13	70
3	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6041168 A	20	High-speed delay verification apparatus and method therefor	716/6	
4	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5798938 A	19	System and method for verification of a precharge critical p	716/6	
5	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5790415 A	19	Complementary network reduction for load modeling	716/6	
6	<input type="checkbox"/>	<input type="checkbox"/>	US 5787008 A	19	Simulation corrected sensitivity	703/19	71
7	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5751593 A	19	Accurate delay prediction based on multi-model analysis	716/6	
8	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5014226 A	19	Method and apparatus for predicting the metastable behavior	716/4	32

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☒ Hit ☐ Details ☐ HTML

Ready

NUM